Docket Number (Optional)	Application Number
POU920030052US1	10/616,556

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Applicant(s)
FADI Y. BUSABA, ET AL.

Filing Date	Group Art Unit
July 10, 2003	2124

(Stamp: OIPE NOV 0 6 2003 PATENT & TRADEMARK OFFICE)

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TM	1	4,390,961	06-28-1983	Negi, et al.	364	756	
	2	4,484,300	11-20-1994	Negi, et al.	364	755	
	3	5,262,976	11-16-1993	Young, et al.	364	760	
	4	5,379,245	01-03-1995	Ueda	364	755	
	5	5,684,731	11-04-1997	Davis	364	754	
	6	6,035,318	03-07-2000	Abdallah, et al.	708	628	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

TM	7	"Packed Decimal Multiply Algorithm": Inventor: Hoffman, RL Schardt, TL: Pubname: TDB 10-75 pgs. 1562-1563; Disclosure No. RO8750134: October 1975
TM	8	"High Performance Two Cycle Loop Decimal Multiply Algorithm"; Inventors: Angiulli, JM Chang, DC Hornick, JC Nohilly, WJ Zajac, MW: Pubname: TDB 09-81 pgs. 1845-1849; Disclsoure No. PO8800209: September 1981

EXAMINER	DATE CONSIDERED
/Tan Mai/	06/27/2006

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION
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	Docket Number (Optional)	Application Number
	POU920030052US1	10/616,556

Applicant(s)
FADI Y. BUSABA, ET AL.

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July 10, 2003	**2124**

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*EXAMINER INITIAL		OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*
	9	IBM Info Gate; "Improved Table Assisted Addition and Multiplication Methods"; Inventor: Wingert, JA; Pubname: TDB 02-83 pgs. 4742-4743; Disclosure No. CT8800050; February 1983
	10	IBM Info Gate; "High Speed Binary and Decimal Multiply"; Inventor: Singh, S. Weinberger, A; Pubname: TDB 05-76 pgs. 4105-4106; Disclosure No. PO8750129; May 1976
	11	"Decimal Adder with Signed Digit Arithmetic"; Inventor: Antonin Svoboda; Ieee Transactions on Computers, Vol. C-18, No. 3, pgs. 212-215, March 1969
	12	"A Signed Binary Multiplication Technique"; Inventor: Andrew D. Booth; Oxford University Press; Q.J. Mech. Appl. Math 4:236-240, pgs. 100-104, 1951

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

P09B/REV04

			Docket Number (Optional) POU920030094US1		Application Number Not Yet Assigned		

INFORMATION DISCLOSURE CITATION
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Applicant(s)
MICHAEL BILLECI ET AL

Filing Date
June 30, 2003

Group Art Unit
Not Yet Assigned

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TM		US2002/0178403 A1	11/28/2002	Floyd et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER /Tan Mai/	DATE CONSIDERED 06/27/2006

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